

The ZAC input common mode range is 1.0V to 2.3V. Input signals referenced at 2.5V must be translated to a reference voltage within ZAC's common mode range. Maximum peak-peak input amplitude of the ZAC is 350mV, which requires the 1V signal to be attenuated to fit this requirement.

A 3-resistor network will fulfill the requirements. It attenuates the 1V to 300mV and translates the reference voltage from 2.5V to 1.65V.

Since single ended operation is required, the negative input terminals are biased at the new reference voltage of 1.65V.

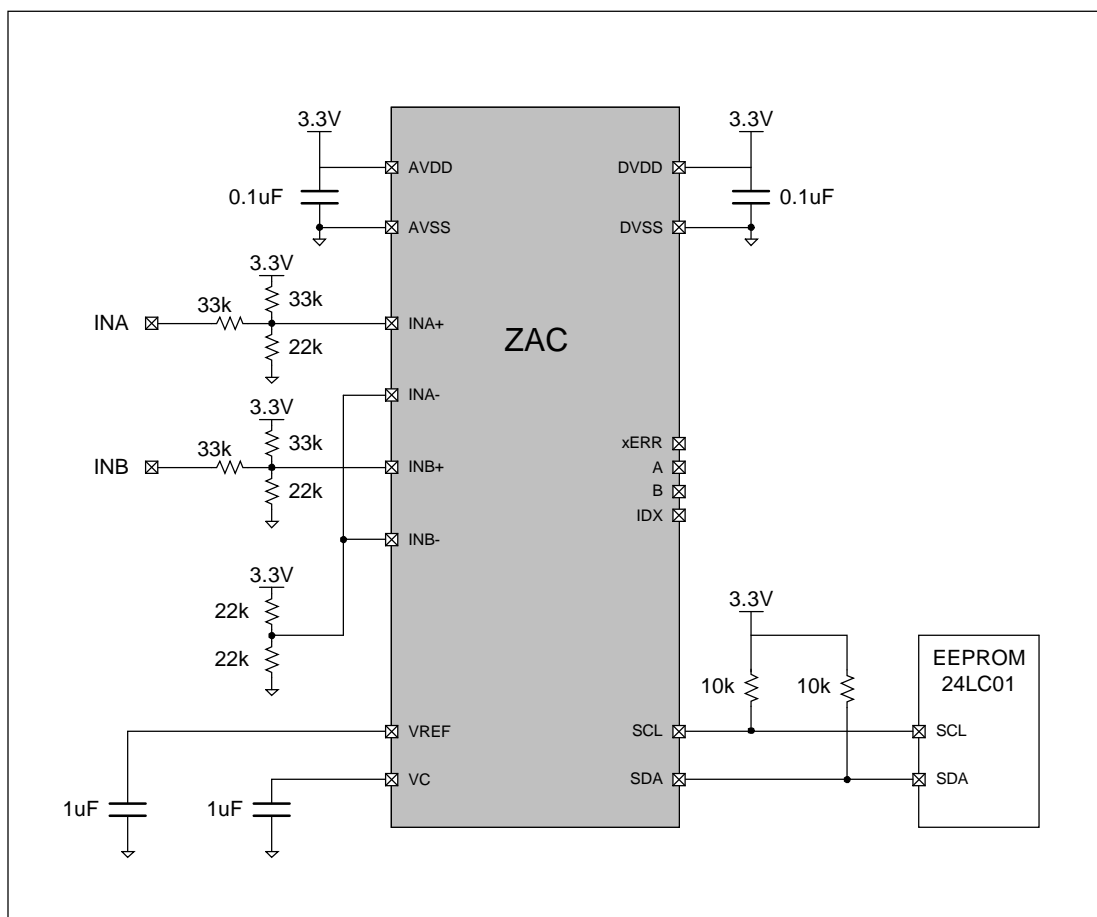


Figure 1: Interface Schematic